

Confirmation No.: 6199

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## UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Thomas J. Massingill, et al.	) CERTIFICATE OF
Confirmation No.: 6199		MAILING/TRANSMISSION (37 C.F.R. § 1.8A)
Serial No.:	09/997,589	I hereby certify that this correspondence is, on the date shown below, being:
Filed:	November 29, 2001	(X) deposited with the United States Postal Service with sufficient postage as first class
For: Multi-Chip Module and Method for Forming and Method for Deplating Defective Capacitors		mail in an envelope addressed to:  Mail Stop Issue Fee  Commissioner for Patents  P.O. Box 1450
Art Group Unit:	1756	) Alexandria, VA 22313-1450
Examiner:	Thai, Luan C.	) ( ) transmitted by facsimile to the Patent and ) Trademark Office.
Atty. Dkt.:	6136-53804 (25916-162)	) 7/23/04 Jordan Wilson

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## LETTER TRANSMITTING FORMAL DRAWINGS TO THE OFFICIAL DRAFTSPERSON

Sir:

Transmitted herewith are formal drawings for the above-identified application bearing Figures 1 - 87 on thirty-four (34) drawing sheets.

By\_

July 23, 2004 SHEPPARD MULLIN RICHTER & HAMPTON LLP Four Embarcadero Center, 17<sup>th</sup> Floor San Francisco, CA 94111-4106 (415) 434-9100 (tel) (415) 434-3947 (fax) Respectfully submitted,

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